

IVEC > High Performance Computing, where to next?



IVEC invites you to a presentation by **Eng Lim Goh, Ph.D., Senior Vice President and Chief Technology Officer, SGI.**

Dr. Goh's talk will cover the **future architectural challenges** for High Performance Computing design and the increasing thirst for large memory, exabytes of data and how to manage this tsunami.

Dr. Eng Lim Goh has been with SGI for 17 years, becoming one of the chief scientists in 1998 and chief technology officer in 2001. His tenure includes work in computer graphics algorithms and high performance computing (HPC) architectures.

In HPC, he oversees Project Ultraviolet, the goal of which is to design and build the company's next generation science and high performance enterprise-driven computer architecture. He is also the coauthor of SGI's recommendation to the high-end computing revitalization task force (HECRTF) for federal funding of key corresponding technologies. This proposal was reviewed by HECRTF in 2003 and judged to be one of the top submitted papers.

He is a proponent of next-generation computer systems designed specifically for customer applications performance. To this, he advocates computational density and energy efficiency when integrating commodity components; and where appropriate, a balanced multi-paradigm approach, across a globally addressable memory, to architectural design.

In computer graphics, Dr. Goh's current research interest is in the relationships between human visual perception and visual computing. He has been awarded two U.S. patents in this field. He is also leading a small research effort to investigate application-transparent, massively parallel rendering.

In 2005, the IDG publication, InfoWorld, named Dr. Goh one of World's 25 most influential CTOs. He has also been named in the HPCwire list of "15 People to Watch."

Before joining SGI, Dr. Goh worked for Intergraph Systems, Schlumberger Wireline Netherlands, and Shell Research U.K. A Shell Cambridge University Scholar, he completed his Ph.D. research and dissertation on parallel architectures and computer graphics. He also holds a first-class honors degree in mechanical engineering from Birmingham University, U.K.

When: Friday 24 November, 2006
10:30am -11am Morning Tea
11am-12pm Seminar

Where: ARRC Auditorium,
(Australian Resources Research
Centre), 26 Dick Perry Avenue,
Kensington, WA

Cost: Free

Registration: Wendy D'Arrigo
admin@ivec.org, by 22 November