iVEC Seminar >



iVEC invites you to a presentation by John Fragalla, High Performance Computing (HPC) Technical Specialist working within the Global Systems Practice for Sun Microsystems, Inc.

Technology innovations and strategies from Sun and the HPC industry > Talk by John Fragella, a senior engineer within Sun responsible for HPC in the research sector.

This seminar will be conducted under a Non Disclosure Agreement (NDA) which will be available for people to sign on the day.

The talk will provide an overview of Sun Microsystems current capability and future plans for HPC; Sun's Intel/AMD Collaboration; Sun's HPC Systems Roadmap, it's future in HPC Interconnect trends & innovation; Filesystem innovations and future directions; Future Storage direction; an overview of Sun's largest HPC Customer - TACC; and the HPC Software Stack and it's future.

As well as many years of practical HPC field experience, John holds a Bachelor of Science in Computer Engineering and a Master of Science in Electrical Engineering, specializing in Computer Architecture and Parallel Computing.

John Fragalla is a High Performance Computing (HPC) Technical Specialist working within the Global Systems Practice for Sun Microsystems, Inc.. John commenced his career at Sun in 1998 as a Systems Engineer working with many of Sun's largest Government and Commercial customers.

Since 2000 John has been intimately involved in with Sun's HPC products and the associated HPC innovation teams. His success in this role lead to his appointment as lead architect for HPC solutions within Sun's Education and Research for Sun in the U.S., until he was promoted to a Global HPC Solutions Architect, where he has worked closely with a number of very large HPC solutions within leading research organisations around the world. Some of the most recent of these include TACC, TITECH &; ARSC.

When: Monday 12 February, 2007 10am -11.30am

Where: ARRC Auditorium. (Australian Resources Research Centre), 26 Dick Perry Avenue, Kensington, WA

Cost: Free

Registration: By 12 February to industry@ivec.org











